

Fig. 1

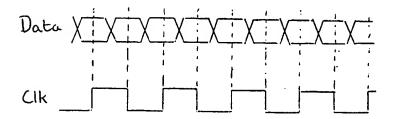


Fig. 2

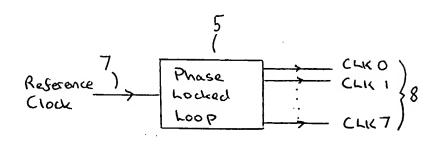


Fig. 3



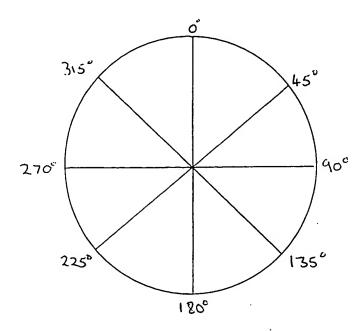


Fig. 4

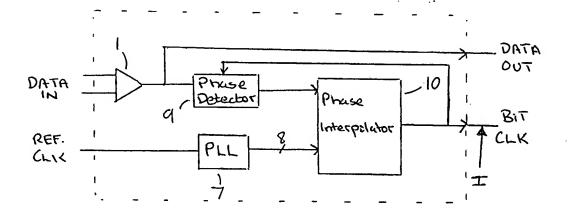


Fig. 5

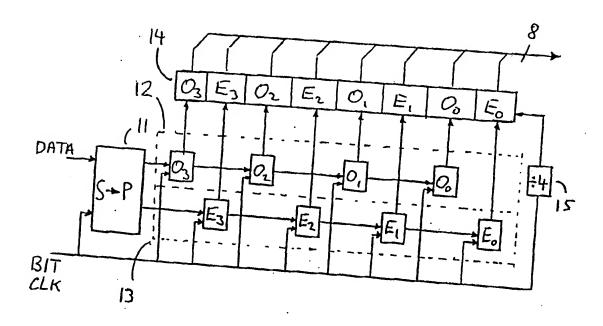
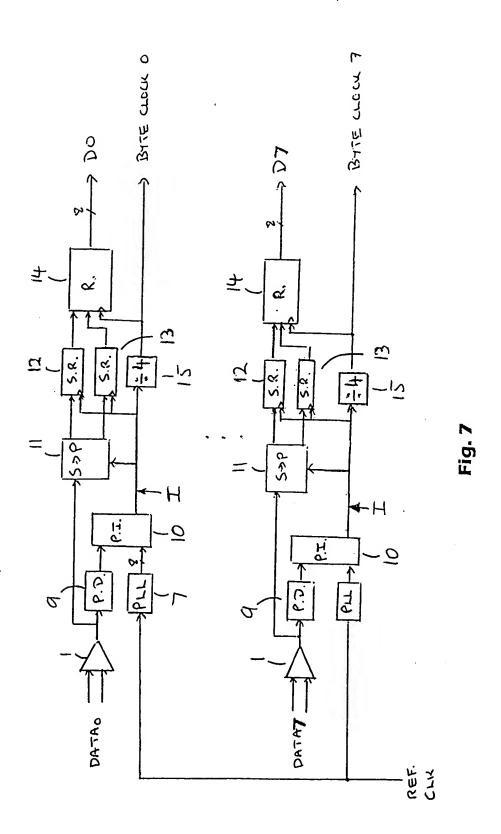


Fig. 6



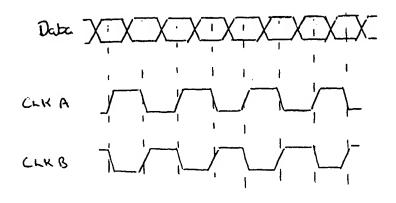
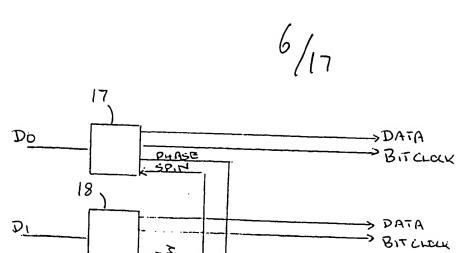


Fig 8



CONTROL

PHASE

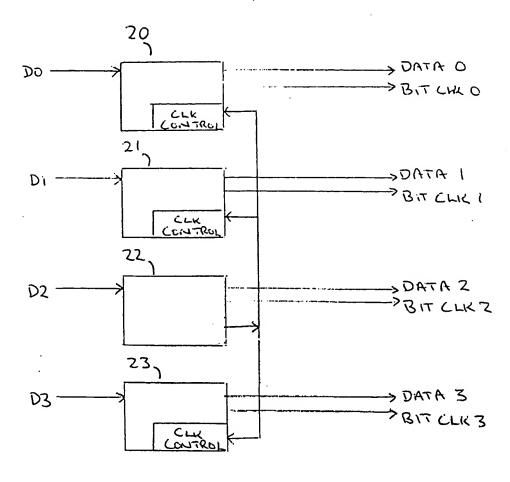
19.

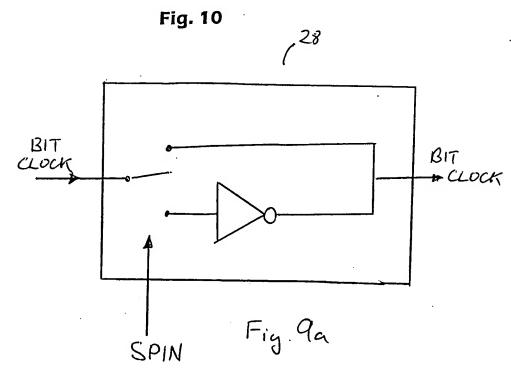
DN

Fig. 9

16

BIT CLOCK





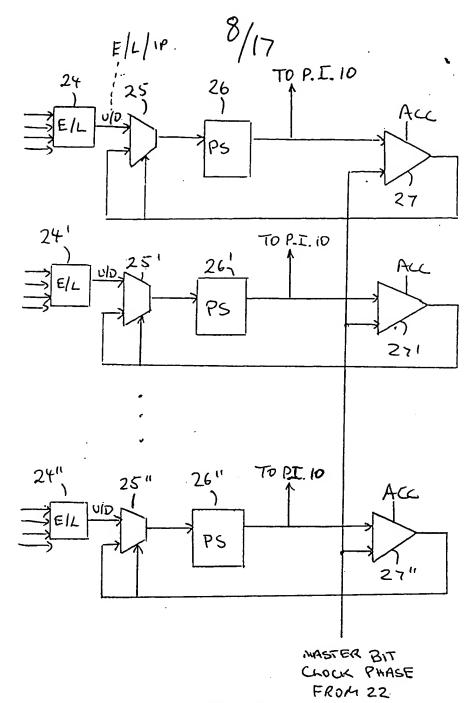


Fig. 11

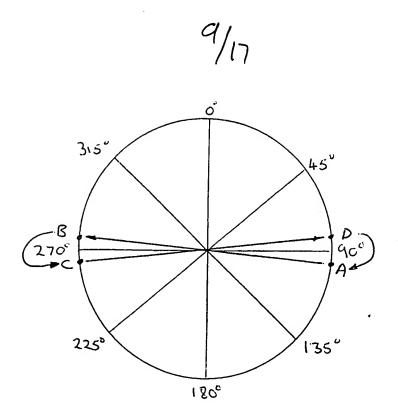


Fig. 12



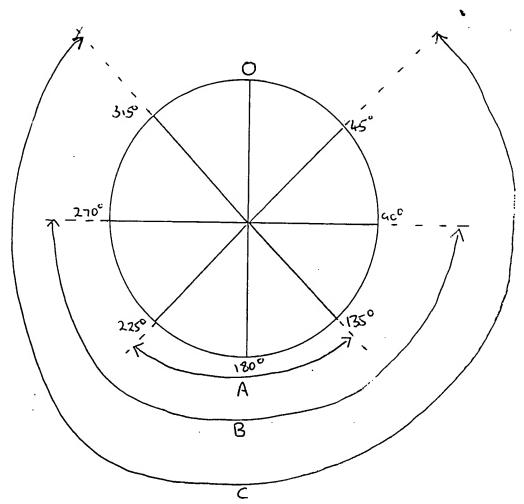


Fig. 13

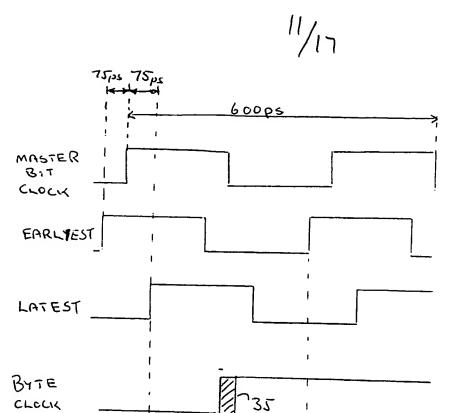
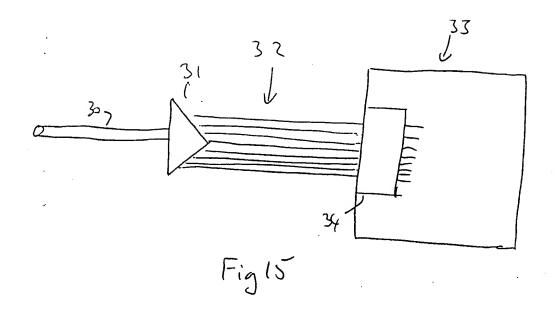
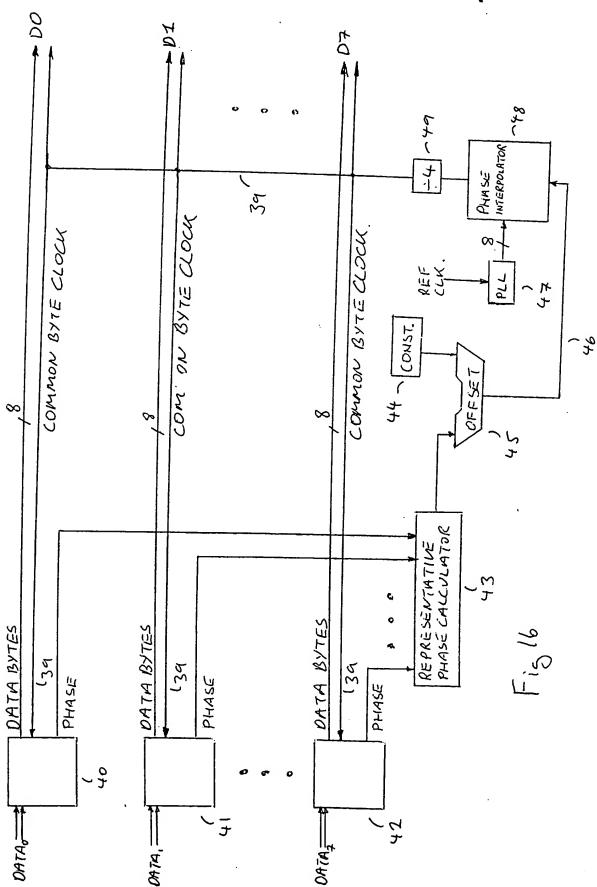
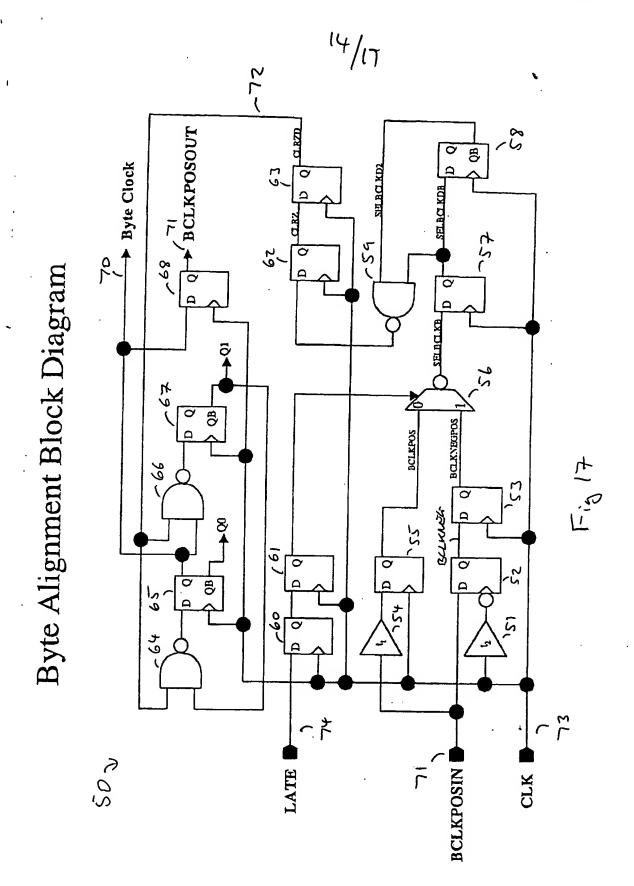
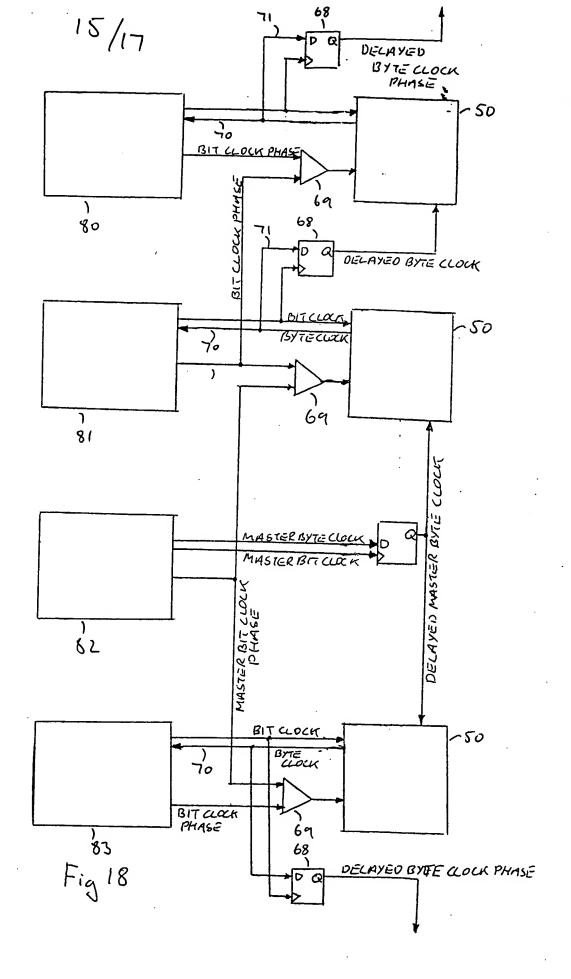


Fig. 14

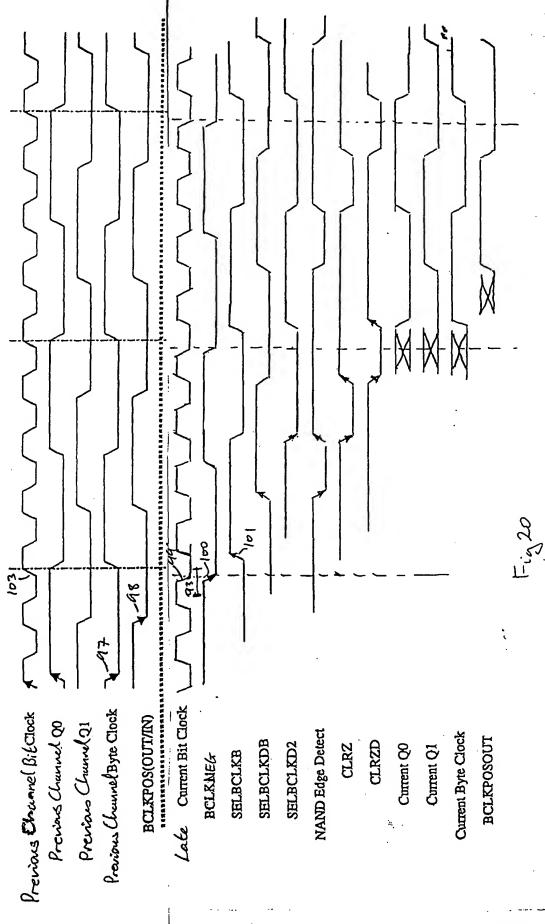








Byte Alignment Timing Diagram - 8 Bit Words



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TI-32316